

Notice of References Cited	Application/Control No. 09/759,414	Applicant(s)/Patent Under Reexamination LI, ZHE	
	Examiner Thomas H. Stevens	Art Unit 2123	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-5,059,897 A	10-1991	Aton et al.	324/538
	B	US-6,057,171 A	05-2000	Chou et al.	438/15
	C	US-6,532,440 B1	03-2003	Boppana et al.	703/14
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	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

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	U	Wan et al., "VHdbx: An X Window System based High-Level Debugger for the VHDL Simulation Environment" 1995 pg.358-360 IEEE Electron Devices Soc.
	V	Li-Z, "Binary Decision Diagram for Computer-Aided Logic Design and Verification" 1991 Dissertation, Syracuse University 152 pages
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.